

Lecture series IC Design

Summer semester 2024



Date	Day	Time	Location	Lecturer	Topic
09.04.2024	Tue	16:40	BAR 213	Achim Graupner, Bosch Sensortec	The Beauty of IC Design
10.04.2024	Wed	16:40	BAR I88	Stefan Getzlaff, Renesas	Analog Frontend Design for Sensor Signal Conditioner
16.04.2024	Tue	16:40	BAR 213	Thomas Klotz, Bosch Sensortec	Advanced verification methods 1
17.04.2024	Wed	16:40	BAR I88	Thomas Klotz, Bosch Sensortec	Advanced verification methods 2
23.04.2024	Tue	16:40	BAR 213	Karsten Einwich, Coseda	Electronic System Level (ESL) Modelling and Simulation
24.04.2024	Wed	16:40	BAR I88	Mario Orgis, Deveritec	Lifetime on an IC from a Managers Perspective
30.04.2024	Tue	16:40	BAR 213	Matteo Michel, IBM	Demystifying Microprocessor Development
01.05.2024	holiday				
07.05.2024	Tue	16:40	BAR 213	Philipp Wartenberg, Fraunhofer IPMS	IC design for microdisplays in AR/VR applications
08.05.2024	Wed	16:40	BAR I88	Thilo Wagner, Racyics	Design for Test (DfT) Concepts for hierarchical IC Designs
14.05.2024	Tue	16:40	BAR 213	Michael Jonas, Infineon Technologies Dresden	Semiconductor Test in Automotive Industry Part I
15.05.2024	Wed	16:40	BAR I88	Paul Jordan, Bastian Cerajewski, DMOS	Semiconductor Test in Automotive Industry Part II
21.05.2024	holiday				
22.05.2024	holiday				
28.05.2024	Tue	16:40	BAR 213	Paul Jordan, Bastian Cerajewski, DMOS	Semiconductor Test - Exercise: analog test concepts and statistics
29.05.2024	Wed	16:40	BAR I88	Christoph Bukethal + Sebastian Simon, Infineon Technologies Dresden	Pre- and Post-silicon Verification of Embedded Power ICs
04.06.2024	Tue	16:40	BAR 213	Stefan Trost, Infineon Technologies Dresden	Semiconductor Test – DfT based Diagnosis
05.06.2024	Dies				
11.06.2024	Tue	16:40	BAR 213	Jeanette Zarbock + Mohammed El-Shennavy, Melexis	Requirement engineering and Functional Safety in Automotive
12.06.2024	Wed	16:40	BAR I88	Love Cederström, Racyics	Power Integrity
18.06.2024	Tue	16:40	BAR 213	Daniel Kollar, Racyics	RTL2GDS ASIC Design Flow - Part 1
19.06.2024	Wed	16:40	BAR I88	Daniel Kollar, Racyics	RTL2GDS ASIC Design Flow - Part 2
25.06.2024	Tue	16:40	BAR 213	Marcel Jotschke, Achim Graupner, Bosch	Device variation: Analog Design Practises for Six-Sigma Yield
26.06.2024	Wed	16:40	BAR I88	Kay-Uwe Giering, Fraunhofer IIS EAS	Overview on integrated-circuit reliability
02.07.2024	Tue	16:40	BAR 213	Andy Heinig, Fraunhofer IIS	Introduction into Advanced Packaging
03.07.2024	Wed	16:40	BAR I88	Andy Heinig, Fraunhofer IIS	Design Space Exploration and Design of Advanced Packages
09.07.2024	Tue	16:40	BAR 213	Gerald Dallmann, SGS Fresenius	Application of physical failure analysis methods for failure localization, defect characterization and layout analysis
10.07.2024	Wed	16:40	BAR I88	Jeanette Zarbock + Sascha Beyer, Melexis	EMC (ElectroMagnetic Compatibility) in Automotive
16.07.2024	Tue	16:40	BAR 213	Menno Mennenga, Marko Noack, FMC	Non-volatile Memory
17.07.2024	Wed	16:40	BAR I88	Sebastian Zwietz, David Ihle, Indie	Power Management in Automotive-ICs

Contact

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Note: BAR stands for Barkhausen-Bau at TU Dresden.